

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Nobuyuki ISHIWATA et al.

Serial No. 09/466,810

GROUP 2754

Filed December 20, 1999

Examiner

MAGNETORESISTANCE APPARATUS  
HAVING REDUCED OVERLAPPING  
OF PERMANENT MAGNET LAYER  
AND METHOD FOR MANUFACTURING  
THE SAME

CLAIM FOR FOREIGN PRIORITY UNDER 35 U.S.C. 119  
AND SUBMISSION OF PRIORITY DOCUMENT

**COPY**

Commissioner of Patents

Washington, D.C. 20231

Sir:

Attached hereto is a certified copy of applicants' corresponding patent application filed in Japan on December 18, 1998, under No. 360159/1998.

Applicants herewith claim the benefit of the priority filing date of the above-identified application for the above-entitled U.S. application under the provisions of 35 U.S.C. 119.

Respectfully submitted,

YOUNG & THOMPSON

By

*for* Robert J. Patch 37,855  
Attorney for Applicants  
Registration No. 17,355  
745 South 23rd Street  
Arlington, VA 22202  
Telephone: 703/521-2297

November 30, 2000

日本国特許庁  
PATENT OFFICE  
JAPANESE GOVERNMENT

US

別紙添付の書類に記載されている事項は下記の出願書類に記載されて  
る事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed  
in this Office.

出願年月日  
Date of Application:

1998年12月18日

願番号  
Application Number:

平成10年特許願第360159号

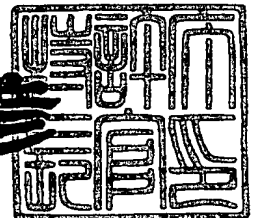
願人  
Applicant(s):

日本電気株式会社

1999年 9月24日

特許庁長官  
Commissioner,  
Patent Office

近藤隆彦



出証番号 出証特平11-306534